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Sheet 1 of 1

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LIST OF REFERENCES CITED BY APPLICANT

*(Use several sheets if necessary)*

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Ming ZHANG et al.

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GROUP ART UNIT

2838

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION		
							YES	NO	PART
QD	1.	WO 02/43232	5/2002	WIPO	H02M	3107	X		

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

	2.	XP-02241539: "An Experimental 1.5V 64-Mb DRAM" by Yoshinobu Nakagome, et al. <u>IEEE Journal of Solid-State Circuits</u> , Vol. 26, No. 4; pages 465-472; April 1991.
	3.	"On-Chip High-Voltage Generation in MNOS Integrated Circuits Using an Improved Voltage Multiplier Technique" by John F. Dickson. <u>IEEE Journal of Solid-State Circuits</u> , Vol. SC-11, No. 3; pages 374-378; June 1976.

EXAMINER	DATE CONSIDERED
Stegrett	4/19/07

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